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501.40792VX1

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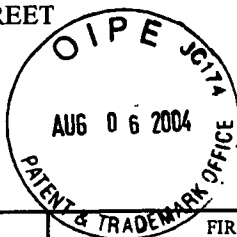
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05/06/2004

ANTONELLI, TERRY, STOUT & KRAUS, LLP
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ARLINGTON, VA 22209-9889



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/997,175	11/30/2001	Yoshihide Yamaguchi	501.40792VX1	5326

TITLE OF INVENTION: A MULTI-CHIP MODULE INCLUDING SEMICONDUCTOR DEVICES AND A WIRING SUBSTRATE FOR MOUNTING THE SEMICONDUCTOR DEVICES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/06/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
VU, HUNG K	2811	257-699000

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(A) NAME OF ASSIGNEE

Hitachi, Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

Gregory E. Montone # 2811 August 6, 2004

(Date)

August 6, 2004

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01 FC:1501

02 FC:1504

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